

Sheet 1 of 1

Form PTO-1449 INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DKT. NO. TAM-103	SERIAL NO. 10/531,287
	APPLICANT T. TANIMOTO, et al.		
	FILING DATE April 14, 2005	GROUP 2825	

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
AA						
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FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Abstract	
						Yes	No
AM							
AN							
AO							
AP							
AQ							
AR							
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AT							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

/SP/	AP	C-based Synthesis Experiences with a Behavior Synthesizer, "Cyber" Wakabayashi, et al., 1999.
/SP/	AQ	IEEE Std 1076, 2000 Edition. IEEE Standard VHDL Language Reference Manual, December 29, 2000.
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/SP/	AV	Kurokawa, H. et al., "C++ Based System Simulator for Pre-Verification of System-on-a-Chip Devices", NEC Research & Development, 07 December, 2000 (07.12.00), Vol. 41, No.3, pages 258-263.
Examiner		/Suchin Parihar/
Date Considered		06/08/2007